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PATENT
2834-0133P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Patentee: GUDENSEN, et al.

Patent No.: 6,776,806 B2

Issue Date: August 17, 2004

Certificate
OCT 20 2004

For: METHOD FOR GENERATING ELECTRICALLY CONDUCTING
AND/OR SEMICONDUCTING STRUCTURES IN TWO OR
THREE DIMENSIONS, A METHOD FOR ERASING THE
SAME STRUCTURES AND AN ELECTRIC FIELD
GENERATOR/MODULATOR FOR USE WITH THE METHOD
FOR GENERATING

ATTN: CERTIFICATE OF CORRECTIONS BRANCH

REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. § 1.322

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 18, 2004

Sir:

Attached hereto is a Certificate of Correction (Form PTO-1050) in connection with the above-identified patent.

It is to be noted that the attached Certificate is being filed in order to correct an error that was clearly made in the printing of the Letters Patent by the U.S. Patent and Trademark Office. Therefore, the issuance of a Certificate of Correction under 35 U.S.C. § 254 without charge to the patentee is respectfully requested.

It is believed that no fee is required. However, if there are any fees chargeable, please charge Deposit Account No. 02-2448.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

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Attachment: Certificate of Correction (Form PTO-1050)

OCT 21 2004

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,776,806 B2

DATED : August 17, 2004

INVENTOR(S): GUDESEN, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please change the front page of the patent as follows:

(30) Foreign Application Priority Data

Jan. 28, 1998	(NO)	980385
Feb. 6, 1998 --June 2, 1998--	(NO)	982518

MAILING ADDRESS OF SENDER:

2834-0133P -MKM/CJB/lab

BIRCH, STEWART, KOLASCH & BIRCH, LLP

PTO BOX 16X

PATENT NO. 6,776,806 B2

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OCT 21 2004